

TENDER

FOR SUPPLY OF

- 1.PADS Professional BND Sw-perpetual-node locked-3L**
- 2. Hyper lynx Thermal AP Sw-perpetual-node locked-1L**

Tender Inquiry No.01 - NTSC/HYD/APADS/2017-18

THE NATIONAL SMALL INDUSTRIES CORPORATION LTD

Technical Services Centre

(A Government of India Enterprise)
Kamalanagar, Kushaiguda, ECIL (Post)
Hyderabad – 500 062

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NOTICE INVITING TENDER

Subject: Supply of PADS professional BND Sw-perpetual-node locked-3L and Hyper lynx Thermal AP Sw-perpetual-node locked-1L

NSIC-Technical Services Centre (NTSC), Hyderabad, A Govt. of India Enterprise, is a reputed Institute setup in 1989 to provide services in various Technical areas thus helping the MSME by providing skilled manpower, common facilities services, Research and Development (R&D) in the field of Electronics and IT.

Sealed quotations are invited in two bids system (Technical & Commercial bid in two separate sealed envelopes contained in one bigger envelope) super-scribed as Supply and installation of **PADS professional BND Sw-perpetual-node locked-3L and Hyper lynx Thermal AP Sw-perpetual-node locked-1L** as detailed below:

Requirement	Supply of PADS professional BND Sw-perpetual-node locked -3L and Hyper lynx Thermal AP Sw-perpetual-node locked -1L, Refer point 7 of Annexure "A" at Page no 6
Technical Bid	The details for submission of Technical Bid is placed at Annexure-`A`
Commercial Bid	The details instruction for submission of Commercial Bid is placed at Annexure-`B`
Cost of Tender Documents	Rs. 500/- (Rupees Five Hundred only) + GST 18% or exempted as detailed in point no 4 of "instructions to the Tenderer" at Page no 3
Earnest Money Deposit along with Tender	Rs.44,250/- (Rupees Forty four thousand two hundred and fifty only) or exempted as detailed in point no 4 "instructions to the Tenderer" at Page no 3
Last date of submission of tender	22ND December 2017 up to 15:00 hours
Date of opening of Technical Bid	22ND December 2017 up to 16:00 hours
Address for Tender inquiry & Delivery	General Manager, NSIC- Technical Services Centre, Kamalanagar, Kushaiguda, ECIL Post, Hyderabad – 500 062

INSTRUCTIONS TO THE TENDERERS

1. The Bidder shall be Authorized Distributors/ Partners/ Dealers of M/S CoreEL Technology (I) Pvt.Ltd currently M/S Mentor Grapics having authorization to supply the product. A copy of valid Authorization letter issued by CoreEL Technology (I)Pvt.Ltd currently Mentor Grapics to Authorized Distributors/ Partners/ Dealers for participating in tender to sale their product shall be enclosed with the Technical bid.
2. The Bidder shall not be a defaulter to any Govt. organization/ PSU since last 2 years for any reason due to non supply of material even after agreed to supply in stipulated time. An undertaking stating the same should be furnished by the bidder on the company letter head as per format enclosed at Annexure "C".
3. The tender documents can be collected from the office of General Manager, w.e.f. 11th December 2017 to 22ND December 2017 (except Saturday & Sunday) between 10:00 to 17:00 hours against payment of **Rs. 500/- (Rupees Five Hundred only) plus 18% GST (Non refundable) by way of Demand Draft, in favor of "NSIC-TSC, Hyderabad" payable at Hyderabad.**

Alternatively tender form can be downloaded from our website www.nsic.co.in from 11th December 2017 to 22ND December 2017. In this case the tender fee in form of DD shall be enclosed with Technical bid.

4. The **Earnest Money Deposit of Rs. 44,250/- (Rupees Forty four thousand two hundred and fifty only) by way of D.D. drawn in favor of "NSIC-TSC, Hyderabad"** shall be submitted along with Technical Bid.

The offers without EMD will be rejected. However, there is exemption of Tender Fee and EMD applicable to the units registered with Central Purchase Organization (DGS&D), National Small Industries Corporation (NSIC) or the concerned Ministry or Department as defined under Rule 157 of General Financial Rules (GFR), 2005. **A copy of valid certificate shall be enclosed along with the Technical bid for availing EMD exemption.**

5. **Tender shall be duly filled in. All pages of the tender form should be signed by the Authorized Signatory of Tenderer with company seal.**
6. Any cutting/over writing in the tender must be signed by the person who is signing the tender. The rates and units shall not be overwritten. **The financial figures in commercial bid shall always be both in figures and words. In case of any discrepancy in figures and words, the amount written in words shall be considered.**

7. The Tenderer shall submit the copy of PAN, GST & TIN registration along with the Technical Bid of the tender document. A Copy of Tax Exemption, or any issued by Taxation Authorities is also need to be enclosed
8. The Tender shall be placed in a properly sealed bigger envelope addressed to General Manager, NSIC-Technical Services Centre, Kamalanagar, Kushaiguda, ECIL Post, Hyderabad - 500 062 and the said **bigger envelope shall contain two separate sealed envelopes containing Technical & Commercial bids.**
9. The bigger envelope shall be super-scribed as "*Tender for the Supply of PADS professional BND Sw-perpetual-node locked -3L and Hyper lynx Thermal AP Sw-perpetual-node locked -1L with its due date. The two sealed envelopes inside the bigger envelope must be super-scribed as: Envelope No-1: The said envelope is for technical bid & shall be super-scribed as "Tender for the Supply of PADS professional BND Sw-perpetual-node locked -3L and Hyper lynx Thermal AP Sw-perpetual-node locked -1L – Technical Bid" and Envelope No-2: The said envelope is for commercial bid & shall be super scribed as "Tender for the Supply of PADS professional BND Sw-perpetual-node locked -3L and Hyper lynx Thermal AP Sw-perpetual-node locked -1L – Commercial Bid".*
10. Technical bid will be opened on 22ND December 2017 at 16:00 hours in the office of General Manager. A representative of the Tenderer may be present at the time of opening of the tender. The date for opening second envelope containing commercial bid shall be intimated to the qualified bidders separately.

We confirm with our acceptance to the Instructions to the tenderer at S.No - 1 to 10 as stated above.

Signature of the Tenderer with seal

TERMS & CONDITIONS

Submission of the duly signed tender implies that these conditions of tender have been read by the Tenderer and is aware of the scope of the supply and the quality of the material to be supplied. The final acceptance of the tender rests with the General Manager of NSIC-Technical Services Centre, who reserves the right to accept or reject any or all tenders without assigning any reason thereof.

- a) No representation for the enhancement of the prices of the accepted tender or alteration of the terms and conditions will be entertained at any stage. Required quantity of software may increase/ decrease.
- b) The delivery period for the supplies to be completed within **60 days** from the date of issue of confirmed supply order from NSIC. In case tenderer fails to supply within the accepted period, General Manager of NSIC-Technical Services Centre, reserves the right to cancel the supply order and may initiate action to debar the supplier for all future business dealing with NSIC for next three years.
- c) The Software License to be supplied shall be valid for Lifetime and perpetual in nature. A document supporting the license type & validity shall be submitted with delivery of license.
- d) Warranty period shall be of minimum one year from the date of satisfactory installation of software.
- e) The rates quoted shall be firm & all inclusive for delivery at destination NSIC Technical Services Centre, Kamalanagar, Kushaiguda, ECIL Post, Hyderabad – 500 062.
90% Payment will be released on successful delivery & installation at our center and balance 10% after expiry of the warranty period.
- f) Validity of tender shall be 60 days from the date of opening of Technical bid
- g) While submitting the tender, if any of the prescribed conditions are not fulfilled or are incomplete in any form, the tender is liable to be rejected.
- h) Any request by the bidders to consider their EMD furnished by them to NTSC for any other contract/ tender cannot be considered as EMD for this tender.
- i) The EMD will be refunded to the unsuccessful bidders after the orders are placed on the successful bidder. In case of successful bidder, the EMD will be refunded along with release of payment for software. No interest on EMD shall be paid.
- j) The EMD will be forfeited in following cases:
 - i. If the bidder fails to accept the order based on his offer within 15 working days.
 - ii. If the bidder fails to supply the software within 60 days from the date of issue of confirmed supply order.
 - iii. If bidder fails to abide by the Terms & Conditions of the award of this tender.
- k) General Manager, NTSC, Hyderabad reserves the right to accept or reject in full or any part of this tender at any stage of this tender process.
- l) In the event of any dispute the legal matter shall be subjected to the jurisdiction of Hyderabad Courts only.

Signature of the Tenderer with seal

Annexure – A`

TECHNICAL BID

1. Tender Ref. No:01/ NTSC/HYD/APADS /2017-18
2. Name of Tenderer:
3. Complete office address of Tenderer.....
.....
4. Contact details of authorized person of tenderer who have signed the tender.
 - a. Name.....
 - b. Designation.....
 - c. Phone (Office).....
 - d. Phone (Mobile).....
 - e. E mail.....
5. EMD payment details (DD number & bank detail)
..... (DD to be attached in original)
6. Tender fee details:
Cash receipt no & date (If taken from our office)
DD number & bank detail (If downloaded from website)
7. Submission of technical specifications confirmation to the requirement.

Sr. No.	Description	Number of License	Confirmation by bidder to supply (Y/N)
1	Pads Professional BND SW-perpetual-node locked	3	
2	Hyper lynx Thermal AP Sw-perpetual-node locked	1	

Submission of Technical Confirmation to the requirement.

Sr. No.	Software Specification	Acceptance of Bidder for supplying the software as specified in Description by writing Yes/No
1	Pads Professional BND SW-perpetual-node locked	
	Archive Management	
	Starter Library	
	PartQuest (Supplier part data)	
	SI Analysis	
	Thermal Analysis	

	Analog Simulation	
	Pads DS Suite	
	Advanced PCB Option	
	Pads Collaborator	
	Advanced Analysis (DDR, IR Drop)	
	Advanced DRC	
	Standalone design review	
2	Hyper lynx Thermal AP Sw-perpetual-node locked	
	Component Temperature Constraint Violations	
	Temperature Gradient	
	Automotive	
	Avionics and Space	
	Computers and Instrumentation	
	Telecom and Industrial Controls	
	Power Supplies	

Technical Specifications of PCB Design Software

Schematic Entry and Circuit Simulation Module:

This tool should facilitate generation of schematics circuit simulation and display of results. It should facilitate to add existing components to schematic and to facilitate creation of non existing component symbols. It should contain component symbols from major manufacturers. During this process the tool should be taken care of defining & modifying various constrains also.

1. Should incorporate searchable symbol library containing more than 1000 parts/symbols and should support creation of new symbols.
2. Component database & schematic should be searchable for different search criteria and should offer suitable components/part alternatives.
3. Should allow to create Flat and multilevel schematics and can name nets automatically while adding the components.
4. Should be able to be corporate component Library and MRP databases through industry-standard ODBC (Open Database Connectivity)
5. Should allow customized Bill of Material (BOM).
6. Allow linking and automatic updating of changes in schematic to simulation tool, pre-layout SI tool as well as Layout tool.
7. Should be able to pass the information such as attributes, footprint information, netlist and design constrains to layout tool.
8. Should facilitate standard DC, Crosstalk, Transient and frequency response analysis.
9. Should allow sweeping through component values with different simulated sources, temperature conditions and comparison of result without interfering with original schematic.

10. Should facilitate generation of simulation report in .txt or .csv format and saving response waveform as image file (JPG/BMP).
11. Should allow editing physical and electrical constraints.
12. Should allow defining at least 50 design rules and perform design rule check (DRC).
13. Should allow Logical definition and management of variants in the Schematic and also able to create variant outputs like BOM, Netlist etc for the Variant schematics.
14. Should be able to generate intelligent Pdf output of the Schematic design.

2.8.1.2. Simulation and Analysis Module

This tool is required for exploring, Optimizing and resolving issues related to electrical Performance at all the stages of design cycle in a high speed (up to 500 MHz), mixed signal (Power, Analog and Digital signals) design environment by enabling electrical constant-driven design flow. Under mentioned are the essential features of this tool:

1. Should allow Signal Integrity Analysis (up to 500 MHz) at **Pre-Layout level** by creating a topology to facilitate experimentation to finalize layout constraints.
2. Should allow Signal Integrity Analysis (up to 500 MHz) at **Post-Layout level** by:
 - 2.1. Modifying the parameters like track length, width, thickness, stack up of layers simultaneously to select the best optimum combination.
 - 2.2. Analyzing and rectifying the signal quality problems such as reflection, ringing, over/under shoots and non-monotonic.
 - 2.3. Deriving suitable impedances of various nets and passive component values
3. Should allow defining custom stimulus for individual net as well as for individual pins.
4. Should allow creating and modifying the stack up sequence of PCB layers.
5. Should allow checking the coupling and Crosstalk between traces running parallel to each other.
6. Should facilitate modelling of dielectric and frequency dependant losses.

2.8.1.3. Layout tool

This tool should facilitate PCB layout, addition of customized footprints along with already existing component footprints, to the circuit layout. The tool should also ensure cross probing with all other design tools and routing of circuit nets through an optimized, Error free and constraint driven process. The Salient Features are:

1. Should have searchable library of component footprints(10000 or more).
2. Should be able to update or download Library symbols, landpatterns and Parts from online portal.
3. Should support SMD, Through Hole IPC 7351 Standard Component families.
4. Should be able to create IPC standard foot prints using templates and also by taking mechanical dimensions of components as input.
5. Should have the capability to generate complex footprints and pin-shapes from DXF file import.
6. Should allow layout of at least 20 layers.
7. Should be able to make and save similar sections as unions in order to allow replacements and replication of sections.
8. Should allow to Cross Probe between schematic and Layout.
9. Should be able to route in any angle direction.
10. Should have ability of self-correcting routes after component movement.

11. Should display guard bands around DRC obstacles while adding or editing traces and should also display the clearance around the selected trace.
12. Should allow passage of trace through the length of passive components.
13. Should be able to define the Board Stack up at the Layout side and forward it to the simulation end and also be able to calculate the trace impedance, trace delay, inductance, and capacitance.
14. Should be able to back annotate changes from layout to the schematic.
15. Should have online DRC feature and also automatic repair of routing violations with respect to the DRC constraints.
16. Should support variants view in Layout as in Schematic and also variant outputs.
17. Should allow blind, buried and micro via assignments.
18. Should allow dynamic copper pour filling.
19. Should support auto routing, auto interactive (User defined) routing, manual/auto tuning of high speed constraint driven signals.
20. Should contain separate tabs in the layout window for Component exploration and net definition.
21. Should allow editing physical, electrical and high speed constraints at the layout end.
22. Should be able to verify the routing checks for clearance, connectivity, high-speed (Minimum/Maximum lengths, Impedance, Delay, parallelism) and plane checks (thermal connectivity).
23. Should support 3D layout functionalities like,
 - 23.1. Component placement and also creation of component unions in order to allow intelligent physical design reuse.
 - 23.2. Separate Constraints and also DRC with respect to 3D aspects.
 - 23.3. Facilitate 3D step models import/export and also 3D pdf export.
24. Should be able to import Gerber, Drill Import and Copper balancing functionality.
25. Should be able to generate Gerber files in all standard formats like RS 274X, RS 274D, NC Drill, ODB++, IPC-2581, GenCAD export.
26. Should support manufacturing output preparation such as fabrication, Assembly drawings, Board dimensioning, addition of special notes, detailed views and Panelization of Layout database.

8. Documents to be enclosed with the Technical bid are as under :

- a. Copy of authorization letter from M/S CoreEL Technology (I) Pvt.Ltd currently M/S Mentor Grapics
- b. Duly signed & stamped all pages of Tender documents as a mark of your acceptance.
- c. Copies of PAN, VAT & TIN
- d. Undertaking on letter head as per Annexure "C".
- e. Document authenticating eligibility towards EMD exemption, if applicable.

Signature of the Tenderer with stamp

FINANCIAL BID

1. Tender Ref. No.:01/ NTSC/HYD/APADS/2017-18
2. Name of the Tenderer:
3. The offer with rates for the schedule of requirements of items, as elaborated under, to be submitted. Adhering to the format given below is a Pre-requisite for considering your quotations:

S.no	Item Description	Number of License	Cost for single license (All inclusive)
1.	Pads Professional BND Sw- perpetual -node locked	3	
2.	Hyper lynx Thermal AP Sw-perpetual-node locked	1	
	Duties and Taxes a) GST@..... Any other tax, indicate with rate of Tax & Charges. b) c) d)		
	Total in Rupees (Figure)		
	Total in Words		
	Total in Words		

4. The above quoted price should be inclusive of basic price with all taxes & duties (as applicable). The price competitiveness shall be given due consideration while analyzing the commercial bid. The NTSC will not issue any form toward rebate / exclusion of Sales tax/GST.

It is hereby confirmed that we shall abide all the terms & conditions as specified in the tender and further agreed for the penalty to be imposed, in case of delayed supplies from our end as specified in Point (b) of "Terms & Conditions" of this tender.

Signature of the Tenderer
With stamp

Undertaking

(On company letter head)

We declare that we are not a defaulter to any Govt. organization/ PSU since last 2 years from the date of issue of this tender no. 01/NTSC/HYD/APADS/2017-18 due to non supply of material for any reason as agreed to supply in stipulated time.

(Authorised Signatory)

******END OF TENDER DOCUMENT******